



## Multitasking in FPGA

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Condition: New. Publisher/Verlag: LAP Lambert Academic Publishing | FPGA-based hardware implementation of multitasking strategies provide high-performance computing that rival ASICs for performance yet provide programmability and ease of development closer to traditional software systems, because of their usefulness, such systems are high in demand and have limited availability. Multitasking can be used to increase the utilization of reconfigurable FPGAs. In this Book a flexible and up-gradable architecture is provided for using the commercially available reconfigurable FPGA's. In this system different Multi Tasking Strategies are implemented using VHDL on FPGA. Because it can take advantage of an application parallelism. By using methods to context switch reconfigurable computing resources, multitasking reconfigurable computing systems can be constructed that provide most of the features available in traditional reconfigurable computers. This Book describes methods to context switch the resources of a reconfigurable computers, as well as a prototype multitasking reconfigurable FPGA that has been created. | Format: Paperback | Language/Sprache: english | 88 pp.



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