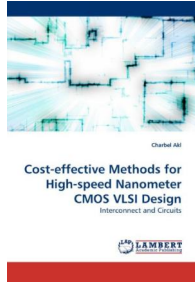


Cost-effective Methods for High-speed Nanometer CMOS VLSI Design



Book Review

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